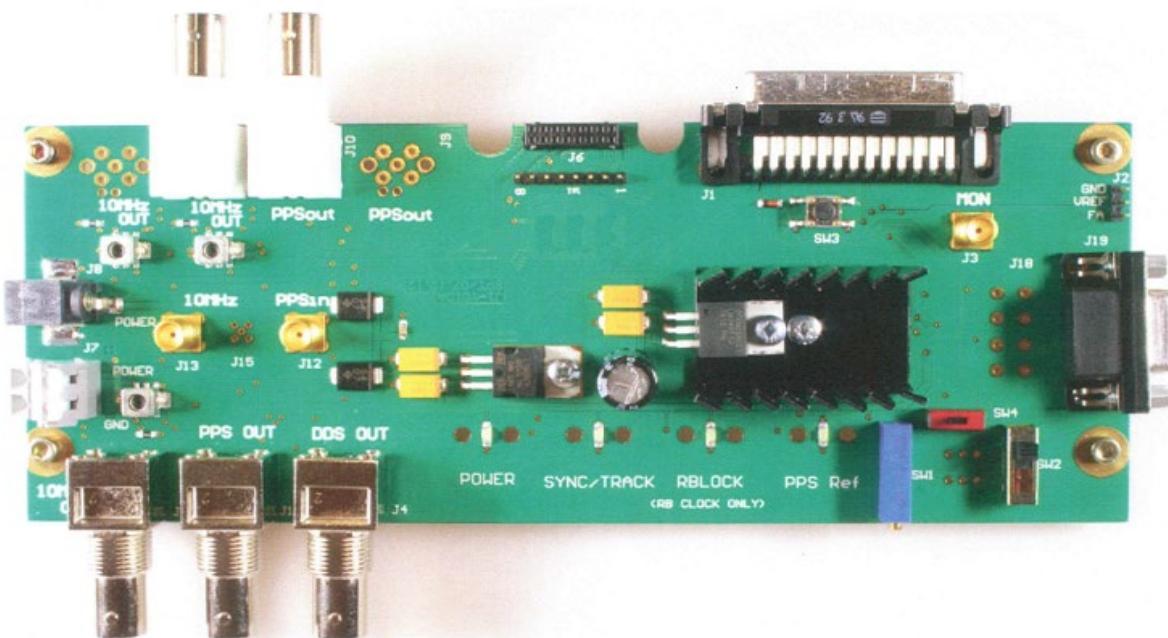


EK-5 Evaluation Kit



User Manual

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1. Board Description

The Rb atoms are confined in a vapor cell at an elevated temperature. The cell is placed in a microwave resonator to which the microwave power derived from the VCXO is coupled. The Rb87 atoms in the cell occur with equal probability in the two hyperfine energy levels of the ground state (F=1 and F=2).

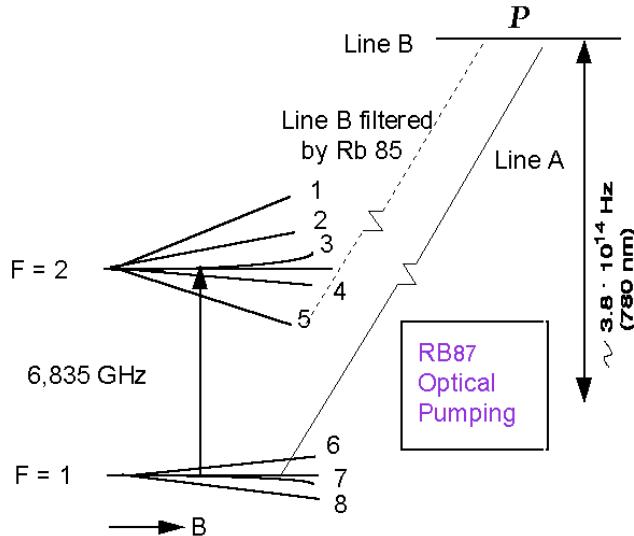
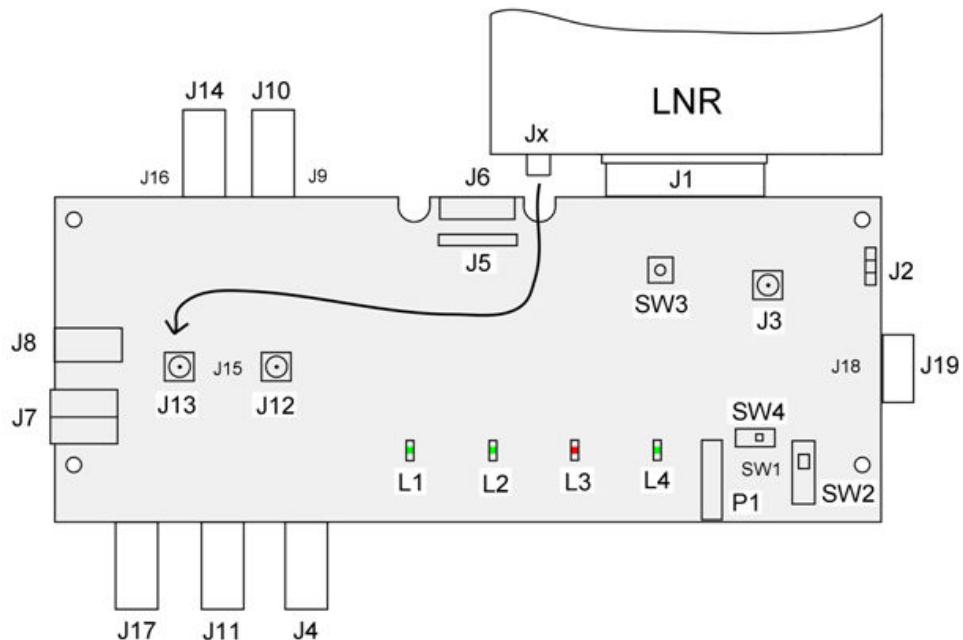


Figure 1: Energy levels and transitions in Rb87 atoms during LPFRS operation.

J1	Connector for LNR	
J2	External frequency adjust of the LNR. FA: connected to pin 6 of the LNR. Vref out: connected to pin 7. GND: ground. In association with switch SW4.	
J3	Internal use at SpT.	Not mounted.
J4	Output for Telecom Option 1 of the LNR.	
J5	LVDS Outputs for the GXClock-500. And 10MHz TTL.	
J6	Connector for GXClock-500.	
J7	Power supply. Pinch connector. GND: ground. Power: +12V or +24V for the LNR; 12V for the GXClock-500.	
J8	Power supply. Jack connector. GND: ground. Power: +12V or +24V for the LNR; 12V for the GXClock-500.	
J9	PPS Output; TTL level.	Not mounted.
J10	PPS Output; TTL level.	
J11	PPS Output; TTL level.	
J12	PPS Ref Input.	
J13	10MHz Input.	
J14	10MHz Output.	
J15	External 10MHz Input in case no LNR and no GXClock-500 are connected.	Not mounted.
J16	10MHz Output.	Not mounted.
J17	10MHz Output.	
J18	Second RS232 connector.	Not mounted.
J19	Serial RS232 interface; to connect the board to a PC with a strait cable.	
SW1	Working mode selector. Rotary switch.	Not mounted.
SW2	Working mode selector. Pos.top: Free run. Pos. Middle: Sync. Pos. Bottom: Track.	
SW3	Reset switch.	
SW4	External frequency adjust source. Left: potentiometer P1. Right: external, connector J2. Only for LNR. Default: Right.	
P1	Potentiometer for frequency tuning. In association with switch SW4. Only for LNR.	
L1	Green Led, power supply ok.	
L2	Green Led, tracking good.	
L3	Red Led, Rubidium line not locked.(LNR)	
L4	Green Led, PPS Ref tick.	

2. LNR CLOK-1500: ACTIVE CONNECTIONS



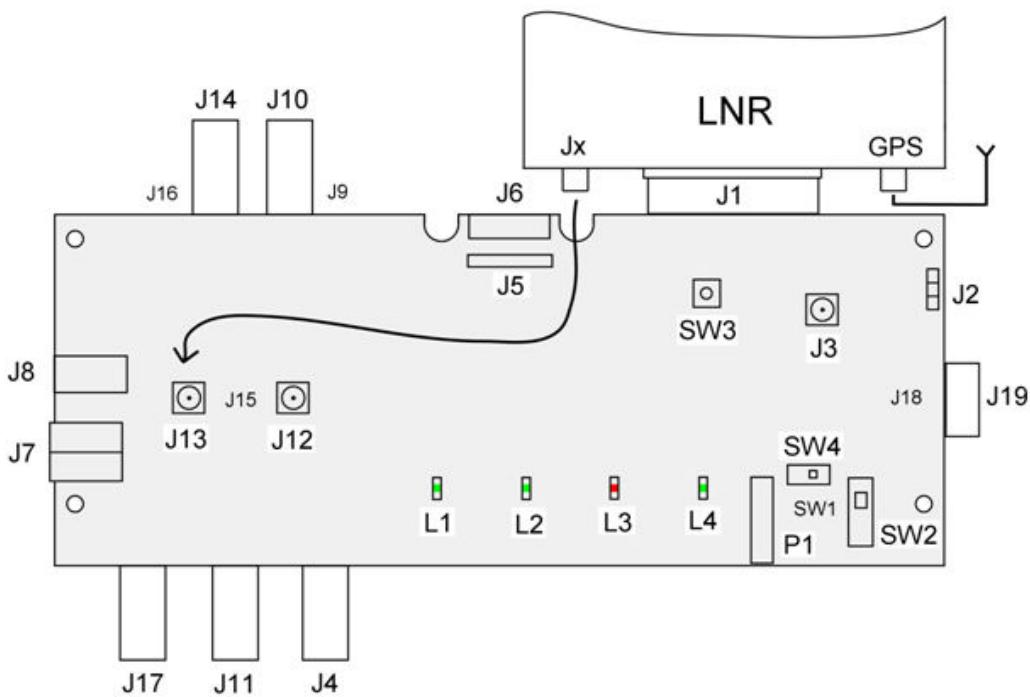
Jx	10MHz SMA Output of the LNR	
J1	Connector for LNR	
J2	External frequency adjust of the LNR. FA: connected to pin 6 of the LNR. Vref out: connected to pin 7. GND: ground. In association with switch SW4.	
J4	Output for Telecom Option 1 of the LNR.	
J5	LVDS Outputs for the GXClock-500. And 10MHz TTL.	Not active.
J6	Connector for GXClock-500.	Some pins active.
J7	Power supply. Pinch connector. GND: ground. Power: +12V or +24V for the LNR; 12V for the GXClock-500.	
J8	Power supply. Jack connector. GND: ground. Power: +12V or +24V for the LNR; 12V for the GXClock-500.	
J10	PPS Output; TTL level.	
J11	PPS Output; TTL level.	
J12	PPS Ref Input.	
J13	10MHz Input.	
J14	10MHz Output.	Active if Jx to J13 cable.
J17	10MHz Output.	Active if Jx to J13 cable.
J19	Serial RS232 interface; to connect the board to a PC with a strait cable.	

SW2	Working mode selector. Pos. top: Free run. Pos. Middle: Sync. Pos. Bottom: Track.	Not mounted.
SW3	Reset switch.	
SW4	External frequency adjust source. Left: potentiometer P1. Right: external, connector J2. Only for LNR. Default: Right.	

P1 Potentiometer for frequency tuning. In association with switch SW4. Only for LNR.

L1	Green Led, power supply ok.	
L2	Green Led, tracking good.	
L3	Red Led, Rubidium line not locked.(LNR)	
L4	Green Led, PPS Ref tick.	Active with J12.

3. LNR CLOK-1500/GPS: ACTIVE CONNECTIONS

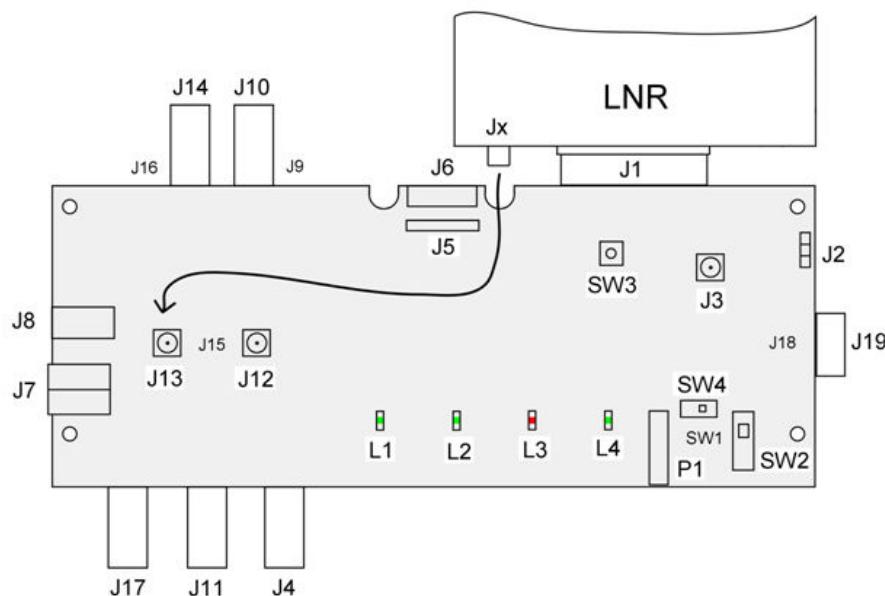


Jx	10MHz SMA Output of the LNR	
J1	Connector for LNR	
J2	External frequency adjust of the LNR. FA: connected to pin 6 of the LNR. Vref out: connected to pin 7. GND: ground. In association with switch SW4.	
J4	Output for Telecom Option 1 of the LNR.	
J5	LVDS Outputs for the GXClock-500. And 10MHz TTL.	Not active.
J6	Connector for GXClock-500.	Some pins active.
J7	Power supply. Pinch connector. GND: ground. Power: +12V or +24V for the LNR; 12V for the GXClock-500.	
J8	Power supply. Jack connector. GND: ground. Power: +12V or +24V for the LNR; 12V for the GXClock-500.	
J10	PPS Output; TTL level.	
J11	PPS Output; TTL level.	
J12	PPS Ref Input.	Not active.
J13	10MHz Input.	
J14	10MHz Output.	Active if Jx to J13 cable.
J17	10MHz Output.	Active if Jx to J13 cable.
J19	Serial RS232 interface; to connect the board to a PC with a strait cable.	

SW2	Working mode selector. Pos.top: Free run. Pos. Middle: Sync. Pos. Bottom: Track.	
SW3	Reset switch.	
SW4	External frequency adjust source. Left: potentiometer P1. Right: external, connector J2. Only for LNR. Default: Right.	

P1	Potentiometer for frequency tuning. In association with switch SW4. Only for LNR.	
L1	Green Led, power supply ok.	
L2	Green Led, tracking good.	
L3	Red Led, Rubidium line not locked.(LNR)	
L4	Green Led, PPS Ref tick.	Active with J12.

4. GRCLOK-1500: ACTIVE CONNECTIONS

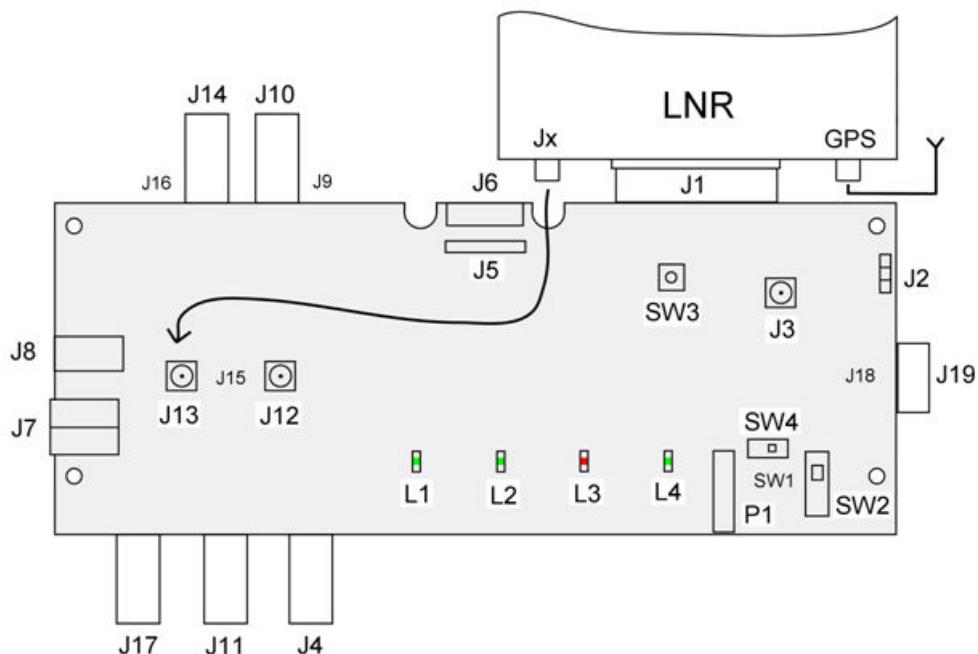


Jx	10MHz SMA Output of the LNR	
J1	Connector for LNR	
J2	External frequency adjust of the LNR. FA: connected to pin 6 of the LNR. Vref out: connected to pin 7. GND: ground. In association with switch SW4.	
J4	Output for Telecom Option 1 of the LNR.	
J5	LVDS Outputs for the GXClock-500. And 10MHz TTL.	10MHz on pin 1.
J6	Connector for GXClock-500.	Some pins active. 10MHz on pin 3.
J7	Power supply. Pinch connector. GND: ground. Power: +12V or +24V for the LNR; 12V for the GXClock-500.	
J8	Power supply. Jack connector. GND: ground. Power: +12V or +24V for the LNR; 12V for the GXClock-500.	
J10	PPS Output; TTL level.	
J11	PPS Output; TTL level.	
J12	PPS Ref Input.	
J13	10MHz Input.	
J14	10MHz Output.	Active if Jx to J13 cable.
J17	10MHz Output.	Active if Jx to J13 cable.
J19	Serial RS232 interface; to connect the board to a PC with a strait cable.	

SW2	Working mode selector. Pos.top: Free run. Pos. Middle: Sync. Pos. Bottom: Track.	
SW3	Reset switch.	
SW4	External frequency adjust source. Left: potentiometer P1. Right: external, connector J2. Only for LNR. Default: Right.	

P1	Potentiometer for frequency tuning. In association with switch SW4. Only for LNR.	
L1	Green Led, power supply ok.	
L2	Green Led, tracking good.	
L3	Red Led, Rubidium line not locked.(LNR)	
L4	Green Led, PPS Ref tick.	Active with J12.

5. GRCLOK-1500/GPS: ACTIVE CONNECTIONS



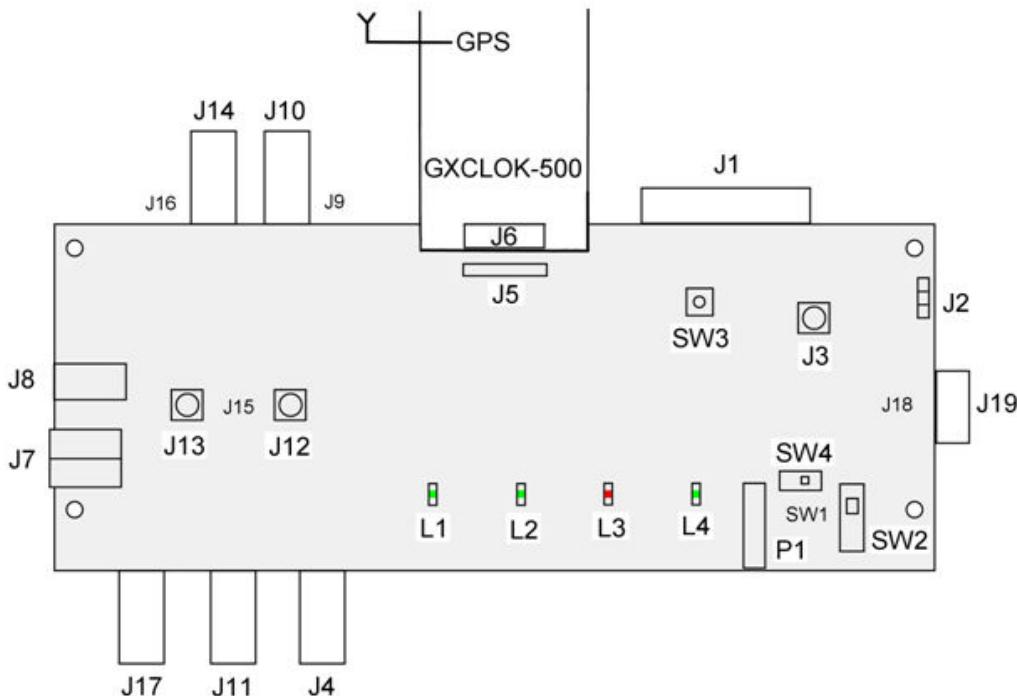
Jx	10MHz SMA Output of the LNR	
J1	Connector for LNR	
J2	External frequency adjust of the LNR. FA: connected to pin 6 of the LNR. Vref out: connected to pin 7. GND: ground. In association with switch SW4.	
J4	Output for Telecom Option 1 of the LNR.	
J5	LVDS Outputs for the GXClock-500. And 10MHz TTL.	10MHz on pin 1.
J6	Connector for GXClock-500.	Some pins active. 10MHz on pin 3.
J7	Power supply. Pinch connector. GND: ground. Power: +12V or +24V for the LNR; 12V for the GXClock-500.	
J8	Power supply. Jack connector. GND: ground. Power: +12V or +24V for the LNR; 12V for the GXClock-500.	
J10	PPS Output; TTL level.	
J11	PPS Output; TTL level.	
J12	PPS Ref Input.	Not active.
J13	10MHz Input.	
J14	10MHz Output.	Active if Jx to J13 cable.
J17	10MHz Output.	Active if Jx to J13 cable.
J19	Serial RS232 interface; to connect the board to a PC with a strait cable.	

SW2	Working mode selector. Pos.top: Free run. Pos. Middle: Sync. Pos. Bottom: Track.	
SW3	Reset switch.	
SW4	External frequency adjust source. Left: potentiometer P1. Right: external, connector J2. Only for LNR. Default: Right.	

P1	Potentiometer for frequency tuning. In association with switch SW4. Only for LNR.	
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L1	Green Led, power supply ok.	
L2	Green Led, tracking good.	
L3	Red Led, Rubidium line not locked.(LNR)	
L4	Green Led, PPS Ref tick.	Active with J12.

6. GXCLOCK-500: ACTIVE CONNECTIONS



J1	Connector for LNR	Some pins active. 10MHz on pin 24 PPS on pin 20.
J2	External frequency adjust of the LNR. FA: connected to pin 6 of the LNR. Vref out: connected to pin 7. GND: ground. In association with switch SW4.	Not active.
J4	Output for Telecom Option 1 of the LNR.	Not active.
J5	LVDS Outputs for the GXClock-500. And 10MHz TTL.	
J6	Connector for GXClock-500.	
J7	Power supply. Pinch connector. GND: ground. Power: +12V or +24V for the LNR; 12V for the GXClock-500.	
J8	Power supply. Jack connector. GND: ground. Power: +12V or +24V for the LNR; 12V for the GXClock-500.	
J10	PPS Output; TTL level.	
J11	PPS Output; TTL level.	
J12	PPS Ref Input.	Activation by software.
J13	10MHz Input.	
J14	10MHz Output.	Not active. Active with J13.
J17	10MHz Output.	Not active. Active with J13.
J19	Serial RS232 interface; to connect the board to a PC with a strait cable.	
SW2	Working mode selector. Pos.top: Free run. Pos. Middle: Sync. Pos. Bottom: Track.	Not active.
SW3	Reset switch.	Not active.
SW4	External frequency adjust source. Left: potentiometer P1. Right: external, connector J2. Only for LNR. Default: Right.	Not active.
P1	Potentiometer for frequency tuning. In association with switch SW4. Only for LNR.	Not active.
L1	Green Led, power supply ok.	
L2	Green Led, tracking good.	
L3	Red Led, Rubidium line not locked.(LNR)	Not active.
L4	Green Led, PPS Ref tick.	Active with J12.

7. Safety!

Warning: Use proper ESD precautions

Caution: Ensure that all cables are properly connected

Handling the product in reasonably foreseeable conditions do not cause any risk for human health, exposure to the SVHC (substances of very high concern) would require grinding the component up.

7.1. Environmental Responsibility

- The equipment contains materials, which can be either re-used or recycled.
- Do not deposit the equipment as unsorted municipal waste. Leave it at an authorized local WEEE collection point or return to Safran to ensure proper disposal.
- To return the appliance:
 - Submit a support ticket at aftersales.clocks@nav-timing.safrangroup.com and request an RMA.
 - We will contact you for more information and/or with shipment process details.

8. Safran Technical Support

For technical support, you can visit <https://safran-navigation-timing.com/support-hub/> to submit a support request.

For product specifications and additional documentation, visit our product page at <https://safran-navigation-timing.com/product/ek-5-evaluation-kit/>

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